



PATENT
81754.0072

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:
Kazuo KOBAYASHI
Serial No: 10/090,483

Confirmation No.: 5100

Filed: March 1, 2002

For: SEMICONDUCTOR INTEGRATED
CIRCUIT FOR SUCCESSIVELY
SCANNING LINES OF
ELECTRODES OF AN IMAGE
DISPLAY APPARATUS

Art Unit: 2673

Examiner: Jeffrey J. Piziali

I hereby certify that this correspondence
is being deposited with the United States
Postal Service with sufficient postage as
first class mail in an envelope addressed
to:

Mail Stop RCE
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

January 21, 2005

Date of Deposit

Rhonda Hurt

Name

Rhonda Hurt

01/21/2005

Signature

Date

PETITION FOR EXTENSION OF TIME

Mail Stop RCE
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

In accordance with 37 C.F.R. 1.136, Applicant respectfully petitions the
Commissioner for a two-month extension of time extending to January 23, 2005, the
period for response to the Final Office Action dated November 23, 2004. A check for
\$450.00 is enclosed. The responsive paper(s) are attached.

Please charge any insufficiency or credit any overpayment to Deposit Account
No. 50-1314. A copy of this petition is enclosed.

Respectfully submitted,
HOGAN & HARTSON L.L.P.

Date: January 21, 2005

By: 

Troy M. Schmelzer

Registration No. 36,667

Attorney for Applicant(s)

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